First Named Inventor Ahn et al.	Unde valid			Application Number	09/754,926
Examiner Name E. Keilin Total Number of Pages in This Submission Atterney Docket Number MI22-1533 ENCLOSURES (check all that apply) Fee Transmittal Form Fee Attached Drawing(s) Assignment Papers (for an Application) Drawing(s) After Final After Final After Final After Final Petition Routing Slip (PTO/SB/69) and Accompanying Petition Provisional Application Power of Attorney, Revocation Change of Correspondence Address Terminal Disclaimer Express Abandonment Request Information Disclosure Statement Request for Refund Response to Missing Parts/ Incomplete Application Response to Missing	T	RANSMITT	AL	Filing Date	January 4, 2001
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		SIGNATURE	OF APPLIC	CANT, ATTORNEY, O	RAGENT
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	Signature Date	tify that this correspondence is	CERTIFICA being deposi	ATE OF MAILING ted with the United States	l. s Postal Service as first class mail in an 2231 on this date: 10/25/2001

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

OCT 2	Application Serial No	09/754,926
	Filing Date	1/4/01
181	Assignee Micron	Technology, Inc.
	Group Art Unit	2813
	Examiner	
	Attorney's Docket No	thods of Forming
	Transistors and Memory Devices	anodo or r orming

RESPONSE TO SEPTEMBER 28, 2001 OFFICE ACTION

To:

From:

Box NON FEE Amendment
Assistant Commissioner for Patents
Washington, D.C. 20231

David G. Latwesen, Ph.D. (Tel. 509-624-4276; Fax 509-838-3424)

Wells, St. John, Roberts, Gregory & Matkin P.S.

601 W. First Avenue, Suite 1300

Spokane, WA 99201-3828

<u>AMENDMENTS</u>

In the Specification

None.

In the Claims

Cancel claims 16-29.